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Docket No.

F0265

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re **PATENT** application of:

Applicant: Feng Pan, et al.

Application No.: 10/050,342

For: METHOD AND APPARATUS FOR PRE-CHARGING NEGATIVE
PUMP MOS REGULATION CAPACITORS

Filing Date: January 16, 2002

Examiner: Thong Q. Le

Art Unit: 2818

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REPLY TO OFFICE ACTION DATED MAY 1, 2002

Box Non-Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Favorable reconsideration of the above-identified application is respectfully requested in view of the following amendments and remarks.

AMENDMENTS

IN THE SPECIFICATION:

On page 12 of the application, please replace the existing paragraph at lines 1-12 with the following replacement paragraph. An appendix illustrating the amended portion of the specification in "marked up" form is provided at the end of this response for the Examiner's ease of reference.

--As illustrated in Fig. 7, ENPGMR going low causes PGMR to go high via gates 220, 222, 224, 226, 228, and 230, and begins a timer circuit (not shown). PGMR going high turns on switch 134a via signal VREFGATE, connecting the divider capacitors C₁ and C₂ with the voltage reference VREF, for example, about 1.3 volts. Referring also to a timing diagram 150 of Fig. 8, about 160ns after PGMR goes high, PGMR again goes low, causing a pulse signal ONESHOT 260 upon a high going pulse T1 from the timer

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